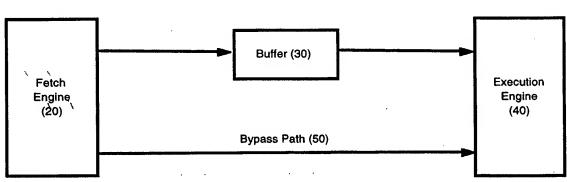
Title: METHOD AND APPARATUS FOR IMPLEMENTING TWO ARCHITECTURES IN A CHIP

Inventor(s): Patrick KNEBEL, et al.

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Attorney Docket No.: 10971390-3





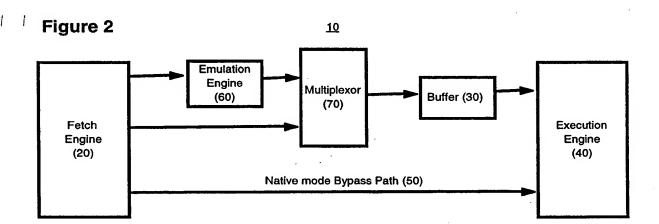
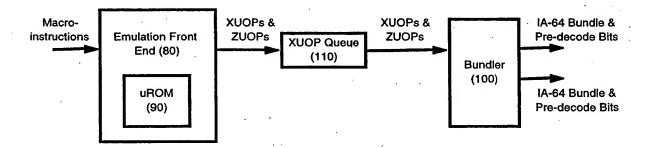


Figure 3 Emulation Engine (60)



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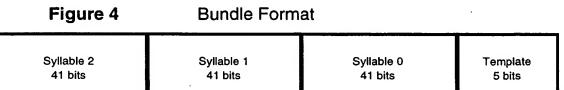


Figure 5 **Bundler Rules** 200 0 entries in Determine 2 entries in XUOP queue Number of XUOP queue Entries In XUOP Queue (0, 1, or 2) 240 1 entry in Does the Bnd XUOP queue Hint Indicate Yes that 2 XUOPs Must Be Issued in Parallel? 220 oes the Bnd Hint Indicate Yes that 2 XUOPs Must Be Issued 260 in Parallel? Do Any of No the Five Rules Apply? No 250 Dispatch 2 210 XUOPs into 2 230 .16-Byte Dispatch 1 **Bundles** Bundler XUOP into 2 Outputs 16-byte Nothing Bundles